

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/614,537 Confirmation No.: 9026
Applicant : Michael R. Butts
Filing Date : July 3, 2003
Title : System And Method For Performing Design Verification
Group Art Unit : 2457
Examiner : Hussein A El Chanti
Docket No. : 700693-4022
Customer No. : 34313

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CHANGE OF CORRESPONDENCE ADDRESS

Sir:

Please change the Correspondence Address for the above-identified patent to:

Cadence Design Systems, Inc.
2655 Seely Avenue, Bldg. 5
San Jose, CA 95134
Telephone: (408) 943-1234
Facsimile: (408) 428-5001

Customer Number: 78167

I am the:

- ☐ Applicant.
- ☐ Assignee of record of the entire interest.
Certificate under 37 CFR 3.73(b) is enclosed.
- ☒ Attorney or agent of record. Registration No. 35,287.

Respectfully submitted,
ORRICK, HERRINGTON & SUTCLIFFE LLP

Dated: August 4, 2010

By: 

Jeffrey A. Miller
Reg. No. 35,287

ORRICK, HERRINGTON & SUTCLIFFE, LLP
4 Park Plaza, Suite 1600
Irvine, CA 92614-2558